# FrelTec GmbH

Mathildenstr. 10A 82319 Starnberg Germany

# Thin Film Chip Resistor SMD Aluminum Nitride

# FrelTec Thin Film Chip Resistors

## SMD

SPECIFICATION

## Part Number

03A	05*	1001*	J*	T05**	D	С
Туре	Size	Value	Tolerance	Packing	TCR	Power Rating
03A : SMD Thin Film	03 : 0603	The last	F : ±1%	T05: Tape and Reel for 5k pc	D :	H: 1/2W
Chip Resistor	05 : 0805	digit is the multiplier	F. ±170	(7"reel) for 0603 to 1206	±25ppm/°C	J: 1W
Aluminum Nitride	06 : 1206	which denotes the		T04: Tape and		L: 2W
	25 ; 2512	number of zero following	D:±0,5%	Reel for 4k pc (7"reel) for 2512	E : ±50ppm/°C	6: 6W
			C:±0,25%			
		Example:	B : ±0,1%			
		97R6=				
		97,6Ohm 9760 =				
		976Ohm 1001 =				
		1kOhm			* not all combination is	
					possible	

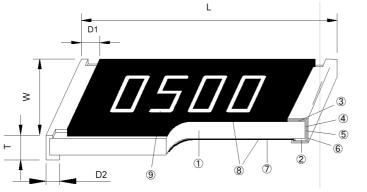
All products according to RoHS (2015/863/EU)

# FrelTec

## SMD THIN FILM CHIP RESISTORS

## Thin Film Chip Resistors

## Construction

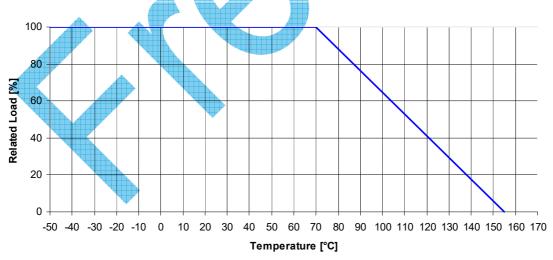


① Alumina Nitride Substrate	③ Edge Electrode	S Resistor Layer	
<sup>©</sup> Bottom Electrode	④ Barrier Layer	6 Overcoat	
<sup>③</sup> Top Electrode	© External Electrode	Marking	

## Dimensions

### **Power Derating Curve**

Size	L	W	T	D1	D2
0603	1,55±0,10	0,80±0,10	0,43±0,15	0,30±0,15	0,50±0,20
0805	2,00±0,15	1,25±0,15	0,43±0,15	0,35±0,15	0,60±0,20
1206	3,05±0,20	1,55±0,20	0,43±0,15	0,50±0,15	1,20±0,20
2512	6,30±0,20	3,10±0,20	0,43±0,15	0,70±0,25	1,60±0,25



# FreITec Thin Film Chip Resistors

## THIN FILM CHIP RESISTORS

038 Series

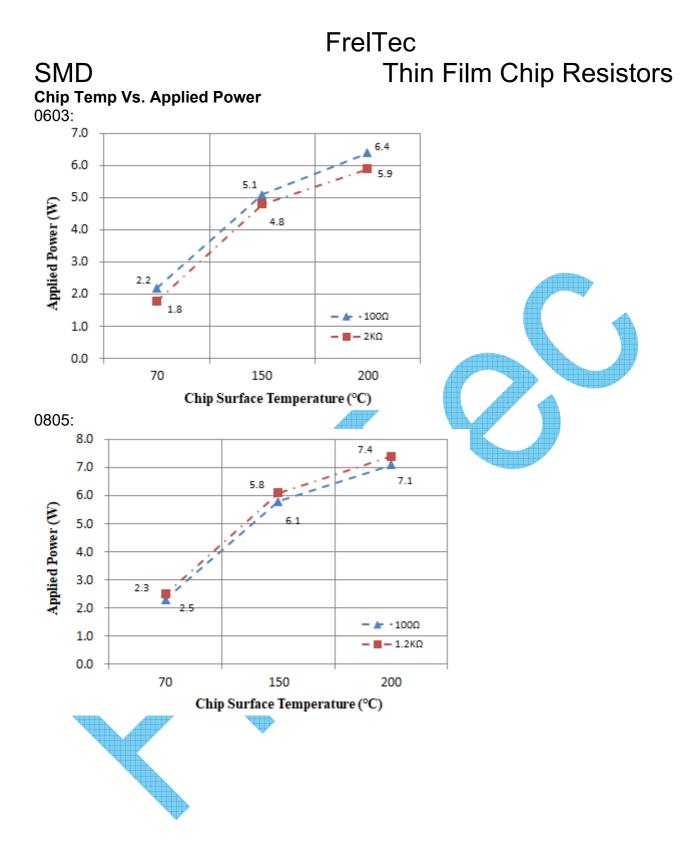
**SMD** 

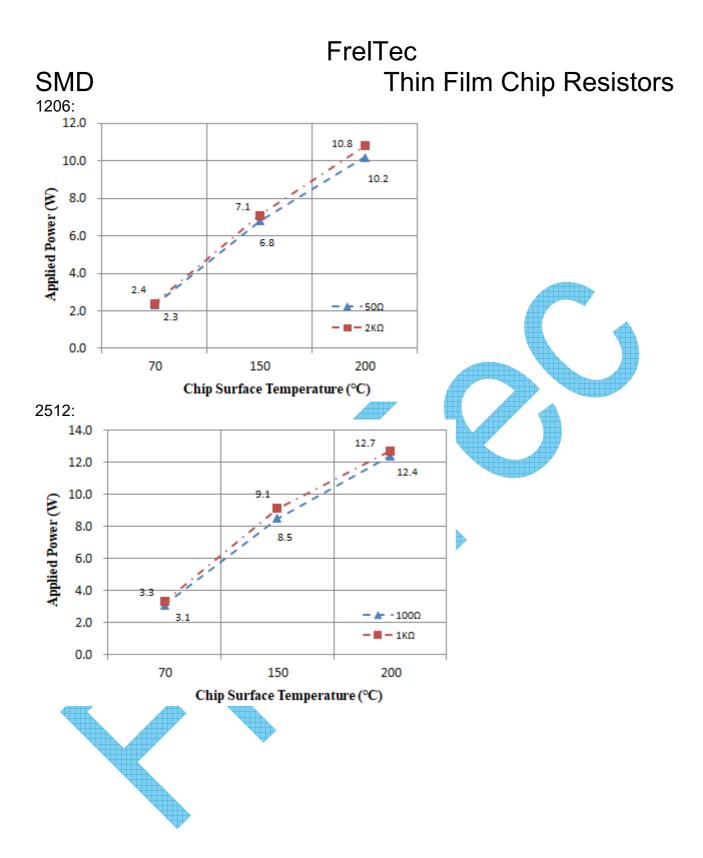
**GENERAL PURPOSE CHIP RESISTORS** 

Туре	Size	Power Rating	Max, Operating	Max, Overload	Operating Coefficient						
Type	OIZC	at 70°C	Voltage	Voltage	Temp. Range	[TCR; ppm/°C]		C(±0,25%) E24, E96*	D(±0,5%) E24, E96*	F(±1%) E24, E96*	
03A 03	0603	1/2W*	75V	75V		±25 ±50		50	Ω~30,1kΩ		
03A 05	0805	1,0W*	100V	200V		±25 ±50		50	Ω~30,1kΩ		
03A 06	1206	2,0W*	100V	200V	+55°C~155°C	±25		50	<b>Ω~30,1k</b> Ω		
03A 25	2512	6,0W*	100V	200V		±50		50	Ω~30,1kΩ		

\* Dependant on component mounting by user. Specific ohm value possible, availability need checking with sales. Chip no martking!

Operating Voltage= $\sqrt{(P^*R)}$  or Max. operating voltage listed above, whichever is lower.





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### SPECIFICATION



#### 0805 to 2512

0603 E-24 series

4 digit marking, first three digits marking are significant figures; forth digit is multiplier ( $10^{X}$ ),

Thin Film Chip Resistors

examples: 1542 = 154x10<sup>2</sup>=15,400 Ohm=15,4kOhm

**FreITec** 

0603 E-96: 3-digit marking

examples: 12C (Table below) = 130×10<sup>2</sup> = 13kOhm

# 512

3 digit marking, first two digits marking are significant figures; third
digit is multiplier (10 <sup>x</sup> ),
examples: 222 = 22×10 <sup>2</sup> = 2,2kOhm

		<u>3 digit N</u>	larking T	able E	<u>96</u>					
Code	E96	Code	E96	Code	E96	Code	E96			
01	100	25	178	49	316	73	562	Â		
02	102	26	182	50	324	74	576			
03	105	27	187	51	332	75	590			
04	107	28	191	52	340	76	604			
05	110	29	196	53	348	77	619			
06	113	30	200	54	357	78	634			
07	115	31	205	55	365	79	649			
08	118	32	210	56	374	80	665			
09	121	33	215	57	383	81	681			
10	124	34	221	58	392	82	698			
11	127	35	226	59	402	83	715			
12	130	36	232	60	412	84	732			
13	133	37	237	61	422	85	750			
14	137	38	243	62	432	86	768			
15	140	39	249	63	442	87	787			
16	143	40	255	64	453	88	806			
17	147	41	261	65	464	89	825			
18	150	42	267	66	475	90	845			
19	154	43	274	67	487	91	866			
20	158	44	280	68	499	92	887			
21	162	45	287	69	511	93	909			
22	165	46	294	70	523	94	931			
23	169	47	301	71	536	95	953			
24	174	48	309	72	549	96	976			
Code	A H	3 C	D	E	F	G	Н	X	Y	Ζ
Multiplier	$10^{0}$ 10	$0^1$ 10	$^{2}$ 10 <sup>3</sup>	3 10	4 105	<sup>5</sup> 10 <sup>6</sup>	107	10-1	10-2	10-3
<u>3 digit Ma</u>	arking Table E	24								
E24 10	11 12 13 15	16 18	20 22 2	4 27 3	30 33 36	39 43	47 51	56 62	68 75	82 91

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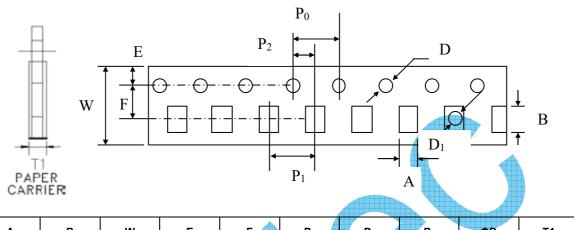
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# SMD

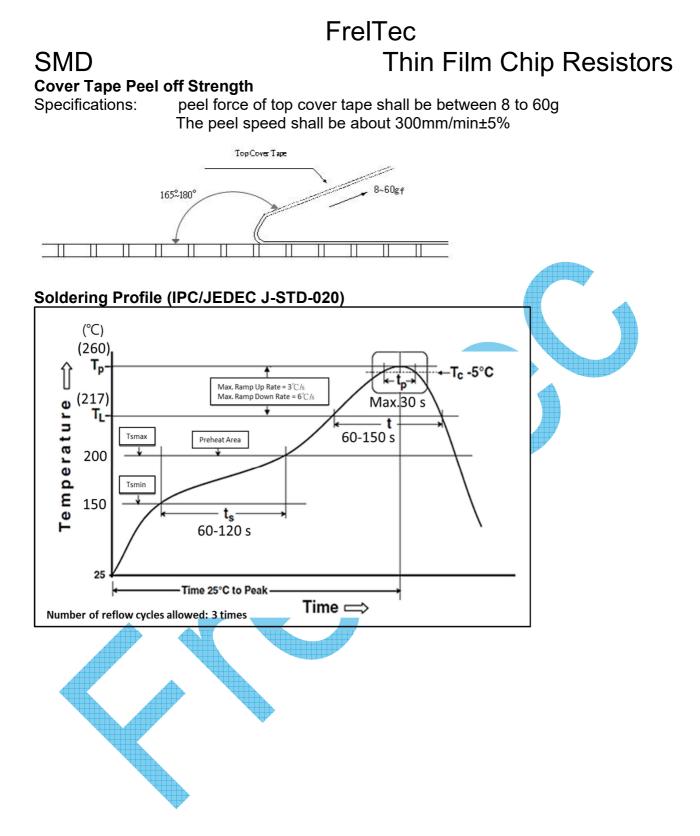
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## SPECIFICATION Tape And Reel Package



	Туре	Α	В	w	E	F	P <sub>0</sub>	P <sub>1</sub>	P <sub>2</sub>	ΦD	T1
	0603	1,10±0,05	1,90±0,05	8,00±0,10	1,75±0,05	3, <b>5±0</b> ,05	4,00±0,10	4,00±0,10	2,00 <mark>±0,0</mark> 5	1,55±0,05	0,60±0,03
	0805	1,60±0,05	2,37±0,05	8,00±0,10	1,75±0,05	3,5±0,05	4,00±0,10	4,00±0,10	2,00 <mark>±0,0</mark> 5	1,55±0,05	0,75±0,05
	1206	2,00±0,05	3,55±0,05	8,00±0,10	1,75±0,05	3,5±0,05	4,00±0,10	4,00±0,10	2,00±0,05	1,55±0,05	0,75±0,05
	2512	3,400±0,10	6,65±0,10	12,00±0,10	1,75±0,10	5,5±0,05	<b>4,00±0,10</b>	4,00±0,10	2,00±0,05	1,50±0,05	1,00±0,20
-											in mm

Туре	Packaging	М	В	D	W
038 03 T05					
038 05 T05 🚿	Paper				9,5±1,0
038 06 T05		178,0±1,0	13,5±0,7	60,0+1,0	
038 25 T05	Embossed				13,5±1,0



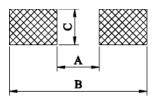
## FreITec Thin Film Chip Resistors

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### Stock period

The performance of these products, including the solderability, is guaranteed for 24 month, provided that they remain packed as they were when delivered and stored at a temperature of  $25^{\circ}C \pm 3^{\circ}C$  and a relative humidity less than 80%RH

## **Recommended Land Pattern Design (mm):**



Size	Α	В	С
0603	0,37	2,35	0,86±0,1
0805	0,50	2,66	1,32±0,1
1206	0,60	4,40	1,80±0,1
2512	2,77	7,39	3,20±0,2

## **Environmental Characteristics**

Item	Requirement	Test Method
Temperature Coefficient of Resistance (T.C.R.)	As spec.	MIL-STD-202 Method 304 +25/-55/+25/+125/+25°C
Short Time Overload	Δ <b>R±0</b> ,5%	Actual power handling capability is limited by the end user mounting process. As with any high power chip resistor the ability to remove the heat is critical to the overall performance of the device
Insulation Resistance	>9999 MΩ	MIL-STD-202 Method 302 Apply 100V <sub>DC</sub> for 1 minute
Endurance	ΔR±1%	MIL-STD-202 Method 108 70±2°C, RCWV for 1000 hrs with 1,5 hrs "ON" and 0,5 hrs "OFF"
Damp Heat with Load	ΔR±0,4%	MIL-STD-202 Method 103 40±2°C, 90~95% R.H. RCWV for 1000 hrs with 1,5 hrs "ON" and 0,5 hrs "OFF"
Solderability	95% min. coverage	MIL-STD-202 Method 208 245±5°C for 3 seconds
Resistance to Soldering Heat	ΔR±0,2%	MIL-STD-202 Method 210 260±5°C for 10 seconds
Low Temperature Operation	ΔR±0,2%	JIS-C-5201-1 4.36 1 hour, -65°C, followed by 45 minutes of RCWV
High Temperature Exposure	ΔR±0,2%	MIL-STD-202 Method 108 At 155°C, for 1000h
Thermal Shock	ΔR±0,2%	MIL-STD-202F Method 107 -55°C ~ 150°C, for 100 cycles

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