FrelTec GmbH

Mathildenstr. 10A 82319 Starnberg Germany

Thick Film Chip Resistor SMD Wide Terminal

Thick Film Chip Resistors

SMD

SPECIFICATION

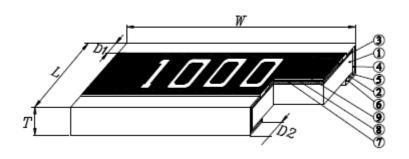
Part Number

022	05*	1001 [*]		J [*]		T05**		С
Туре	Size	Value		Tolerance		Packing		Power Rating
022 : SMD Thick Film Chip Resistor Wide Terminal	A5 : 0508	The last digit is the multiplier		J : ±5%		T05: Tape and reel for 5k pc (7"reel) 0508 and 0612 size	4	1: 3/4W
	62 : 0612	which denotes the number of zero following	4	F: ±1%		E04: Tape and reel for 4k pc (7"reel) 1020 and 1225 size		J: 1W
	A2 : 1020	0000=0Ohm		D: ±0,5%				K: 1,5W
	15 : 1225				-			L: 2W
		Example:	4					M: 3W
		97 R6= 97,6 O hm						
		9760 = 976Ohm						
		1001 = 1kOhm						
		E24-Series is first digit "0"	*					
				* not all combination is possible				

All products according to RoHS (2015/863/EU)

FrelTec Thick Film Chip Resistors

SMD THICK FILM CHIP RESISTORS



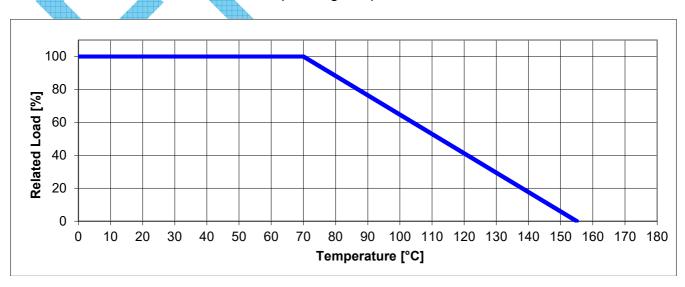
① Alumina S	ubstrate @	Edge Electrode	7	Resistor Layer
② Bottom El	ectrode	Barrier Layer	8	Primary Overcoat
③ Top Election	ode ©	External Electrode	9	Secondary Overcoat

Dimensions

			4000000 VG00000.		
SIZE	L	W	T	D1	D 1
0508	1,25±0,10	2,00±0,15	0,55±0,10	0,30±0,15	0,30±0,15
Jumper	1,23±0,10	2,00±0,13	0,55±0,10	0,20±0,15	0,30±0,13
0612	1,55±0,10	3,00±0,15	0,55±0,10	0,25±0,15	0,40±0,15
Jumper	1,55±0,10	3,00±0,13	0,55±0,10	0,23±0,13	0,40±0,15
1020	2,45±0,15	5,00±0,10	0,60±0,15	0,35±0,20	0,70±0,20
Jumper	2,45±0,15	3,00±0,10	0,0010,13	0,45±0,20	0,70±0,20
1225	3,20±0,20	6 10-0 15	0,65±0,15	0,40±0,20	1,10±0,20
Jumper	3,20±0,20	6,40±0,15	0,00±0,10	0,50±0,20	0,70±0,20

Power Derating Curve

For all resistors operated in ambient temperatures above 70°C, power rating must be derated in accordance with the curve below. Operating temperature -55°C to +155°C



2/14/2022 © FrelTec® GmbH (unit: mm)

SMD

Thick Film Chip Resistors

THICK FILM CHIP RESISTORS

Rating

014 Series

GENERAL PURPOSE CHIP RESISTORS

Standard Type

Туре	Size	Power Rating at 70°C	Max. Operati ng Voltage	Max. Overlo ad Voltag e	Operating temperature	Temperature Coefficient [TCR; ppm/°C]	Resistance Range [Ω] F(±1%) F(±1%) J(±5%) E-24, E-96 E-24								
022 A5	0508	3/4W	200V	400V		±150	1Ω≤R≤9,76Ω								
022 70	0000	0/4 * *	200 V	700 V		±100	10Ω≤R≤1MΩ								
022 62	0612	1W	200V	400V		±100	1Ω≤R≤1ΜΩ								
													-55 ~ +155°C	±150	1Ω≤R≤9,76Ω
022 A2	1020	1,5W	200V	400V		±100	10Ω≤R≤1MΩ								
044.45	4005	0)4/	0001/	400)/		±200	1Ω≤R≤29,4Ω								
014 15	014 15 1225 2W 200V 400	400V		±100	30Ω≤R≤1MΩ										

High Power - Type

Туре	Size	Power Rating	Max. Operating	Max. Overl oad	Operating	Temperature Coefficient	Resistan	ce Range [٩	Ω]	Jump er Rated	Jumper Resistance
,		at 70°C	Voltage	Volta ge	temperature	[TCR; ppm/°C]	F(±1%) E-24, E-96	F(±1%) E-24, E- 96	J(±5 %) E-24	Curre nt	Value J
022 A5	0508	1W	200V	400V		±150	1Ω≤l	R≤9,76Ω		5A	10mΩ
022 A3	0308	1 V V	200 V	400 V		±100	10Ω:	≤R≤1MΩ		SA	Max
022 62	0612	1,5W	200V	400V	-55 ~	±100	1Ω≤	R≤1MΩ		6A	10mΩ Max
000 40	1000	0)4/	000) (400) (+155°C	±150	1Ω≤l	R≤9,76Ω		404	10mΩ
022 A2	1020	2W	200V	400V		±100	10Ω:	≤R≤1MΩ		10A	Max
014 15	1225	3W	200V	400V		±200	1Ω≤l	R≤29,4Ω		12A	10mΩ
014 15	1223	SVV	200 V	400 V		±100	30Ω:	≤R≤1MΩ		IZA	Max

Operating Voltage= $\sqrt{(P^*R)}$ or Max. operating voltage listed above, whichever is lower. Overload Voltage=2,5* $\sqrt{(P^*R)}$ or Max. overload voltage listed above, whichever is lower.

Marking

1%: 4 digit marking

Example: 20 Ohm Marking 20R0

100 Ohm Marking 1000 1kOhm Marking 1001

5%: 3 digit marking in E24:

E24 code 10 11 12 13 15 16 18 20 22 24 27 30 33 36 39 43 47 51 56 62 68 75 82 91

Example: 100 Ohm Marking 101

1kOhm Marking 102

2/14/2022 © FrelTec® GmbH 4/8 www.freltec.com

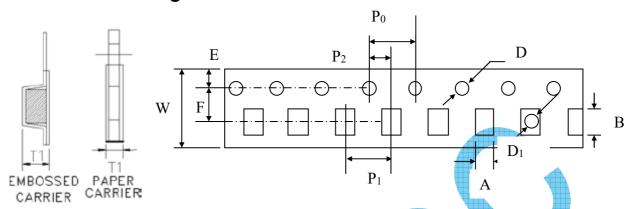
Please read cautions and warnings and important notes at the end of this document.

SMD

Thick Film Chip Resistors

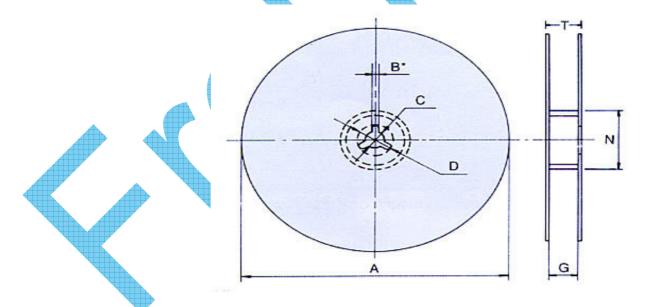
SPECIFICATION

Tape And Reel Package



Packing	Size	Α	В	W	E	F	P ₀	P ₁	P_2	D	D ₁	T ₁
Paper	0508	1,60±0,10	2,40±0,20	8,0±0,20		3,50±0,0	4,00±0,10	4,00±0,05		1,50+0,1	_	0,85±0,10
Tape	0612	1,90±0,10	3,50±0,20	0,010,20		5	4,0010,10	4,0010,03		-0		0,03±0,10
Embossed	1020	2,80±0,15	5,40±0,20	12,0±0,1	1,75±0,10		4,00±0,05	4,00±0,10	2,00±0,05	1,55±0,10	1,5+0,25	1,00±0,20
Tape	1225	3,50±0,10	6,70±0,10	0	A	5,5±0,05	4,00±0,05	4,00±0,10		1,55±0,10	-0	1,00±0,20

Reel size



Sym	nbol	Reel Type / Tape	Α	N	С	G	Т
	Paper Tape	7" reel for 8 mm Tape				9,0±0,5	12,5±0,5
Dimension	Embossed Tape	7" reel for 12 mm Tape	178,5±1,5	60,0+1 -0	13,0±0,2	13,0±0,5	15,5±0,5

in mm

Stock period

The Temperature condition must be controlled at 22± 6 °C, the R.H. must be controlled at less than 80%. The stock can maintain quality level in 12 month.

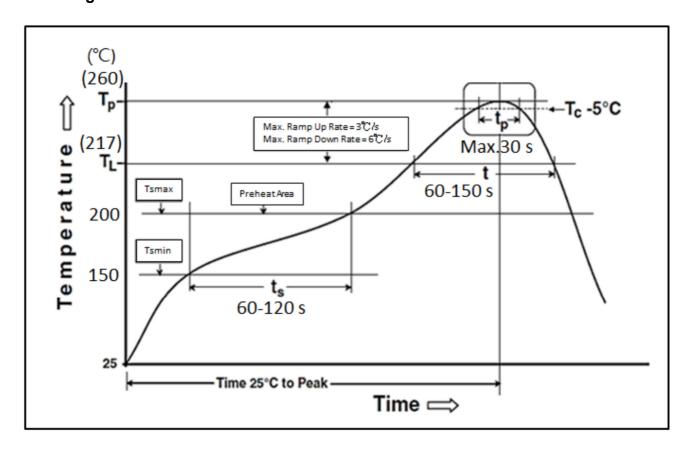
2/14/2022

5/8

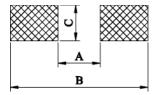
© FrelTec® GmbH

www.freltec.com

Soldering Profile IPC/JEDEC J-STD-020



Recommended Land Pattern Design:



Size	Α	В	С
0508	0,55	2,35	2,00
0612	0,70	2,30	3,20
1020	1,00	3,40	5,00
1225	1,00	5,00	7,00

SMD

Thick Film Chip Resistors

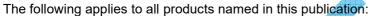
Environmental Characteristics

Item	Requirement	Test Method				
Temperature Coefficient of Resistance (T.C.R.)	As Spec.	JIS-C-5201-1 4.8 IEC-60115-1 4.8 25°C/-55°C~25°C/+125°C, 25°C is the reference temperature				
Short Time Overload	±(1,0%+0,05Ω)	JIS-C-5201-1 4.13 IEC-60115-1 4.13 RCWV*2,5 or Max. overload voltage whichever is lower for 5 seconds				
Insulation Resistance	≥10G	JIS-C-5201-1 4.6 IEC-60115-1 4.6 Max. overload voltage for 1 minute				
Endurance	±(1,0%+0,10Ω)	JIS-C-5201-1 4.25 IEC-60115-1 4.25.1 70±2°C, RCWV for 1000 hrs. with 1,5 hrs. "ON" and 0,5 hrs. "OFF"				
Damp Heat with Load	±(1,0%+0,10Ω)	JIS-C-5201-1 4.24 IEC-60115-1 4.24 40±2°C, 90~95% R.H. RCWV for 1000 hrs. with 1,5 hrs. "ON" and 0,5 hrs. "OFF"				
Dry Heat	±(1,0%+0,05Ω)	JIS-C-5201-1 4.23 IEC-60115-1 4.23.2 at +125/+155°C for 1000 hrs.				
Bending Strength	±(1,0%+0,05Ω)	JIS-C-5201-1 4.33 IEC-60115-1 4.33 Bending once for 60 seconds with 3mm				
Solderability	95% min. coverage	JIS-C-5201-1 4.17 IEC-60115-1 4.17 245±5°C for 3 seconds				
Resistance to Soldering Heat	±(0,5%+0,05Ω)	JIS-C-5201-1 4.18 IEC-60115-1 4.18 260±5°C for 10 seconds				
Voltage Proof	No breakdown or flashover	JIS-C-5201-1 4.7 IEC-60115-1 4.7 1,42 times Max. Operating Voltage for 1 minute				
Leaching	Individual leaching area ≦5% Total leaching area ≤ 10%	JIS-C-5201-1 4,18 IEC-60068-2-58 8.2.1 260±5°C for 30 seconds				
		JIS-C-5201-1 4.19 IEC-60115-1 4.19 -55°C to +125/+155°C, 5 cycles				

SMD

FrelTec Thick Film Chip Resistors

Published by FreITec® GmbH
Mathildenstr. 10A; 82319 Starnberg; Germany
© 2022 FreITec® GmbH. All Rights Reserved.



- 1. The information describes the type of component and shall not be considered as assured characteristics.
- 2. Terms of delivery and rights to change design reserved.
- 3. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. Nevertheless, we explicitly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, FreITec® is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a FreITec® product with the properties described in the product specification is suitable for use in a particular customer application.
- 4. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
- 5. The warnings, cautions and product-specific notes must be observed.
- 6. In order to satisfy certain technical requirements, some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as "hazardous"). Useful information on this will be found in our Material Data Sheets. Should you have any more detailed questions, please contact our sales offices.
- 7. We constantly strive to improve our products. Consequently, the products described in this publication may change from time to time. The same is true for the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order. We also reserve the right to discontinue production and delivery of products. Consequently, we cannot guarantee that all products named in this publication will always be available.
- 8. Unless otherwise agreed in individual contracts, all orders are subject to the current version of the "General conditions for the supply of products and services of the electrical and electronics industry" published by the German Electrical and Electronics Industry Association (ZVEI), available at www.freltec.com.
- 9. As far as patents or other rights of third parties are concerned, liability is only assumed for components per se, not for applications, processes and circuits implemented within components or assemblies.
- 10. The trade name FrelTec® is a trademark registered or pending in Europe and in other countries.

2/14/2022 8/8 © FrelTec® GmbH www.freltec.com